

# Quartus II Foundation Workshop



## Course Description

This workshop gives a comprehensive introduction into designing with Altera design software and Altera programmable logic devices. It is suitable for engineers who are new to programmable logic design or who have already designed some logic and would like to gain a deeper understanding of the design flow, the available tools and how to control specific device features. The workshop is a combination of lectures and hands-on exercises.

## Skills Developed

- Understand programmable logic architectures
- Understand the programmable logic design flow
- Learn golden design rules for programmable logic
- Manage Quartus II projects
- Work with different design entry options
  - Schematic design entry
  - HDL design entry
  - Mega-Wizard
  - SOPC Builder
- Simulate a design in Quartus II
- Plan & manage device I/O assignments using Pin Planner
- Analyze clock & input/output timing using TimeQuest
- Review compilation results

## Skills Required

- Background in digital logic design
- Experience with PCs and the Windows operating system

Course Length	3 days	
Language	Presentation in German or English Slides and documentation in English	
Platform	PC Windows XP / Windows 7	
Pricing	Public In-House	1500,- EUR/attendee On Request
Dates	See schedule at <a href="http://www.elcamino.de">http://www.elcamino.de</a>	

## Exercises

- Create a project using the New Project Wizard
- Design Entry
- Compilation
- Revisions and Assignments
- I/O Assignments
- Timing Assignments
- Simulation
- Debugging with In-System Sources & Probes and SignalTap II

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