

Quartus Prime Foundation Workshop



Course Description

This workshop gives a comprehensive introduction into designing with Intel® FPGA design software and Intel programmable logic devices. It is suitable for engineers who are new to programmable logic design or who have already designed some logic and would like to gain a deeper understanding of the design flow, the available tools and how to control specific device features. The workshop is a combination of lectures and hands-on exercises.

Skills Developed

- Understand programmable logic architectures
- Understand the programmable logic design flow
- Learn golden design rules for programmable logic
- Manage Quartus Prime projects
- Work with different design entry options
 - Schematic design entry
 - HDL design entry
 - IP Catalog
 - Platform Designer (fka Qsys)
- Simulate a design with ModelSim
- Plan & manage device I/O assignments using Pin Planner
- Analyze clock & input/output timing using TimeQuest
- Review compilation results
- Work with Debugging Tools
 - Sources & Probes
 - SignalTap

Skills Required

- Background in digital logic design
- Experience with PCs and the Windows operating system

Course Length	3 days
Language	Presentation in German or English Slides and documentation in English
Platform	PC Windows 10
Pricing	Public 1500,- EUR/attendee In-House On Request
Dates	See schedule at http://www.elcamino.de

Exercises

- Create a project using the New Project Wizard
- Design Entry
- Compilation
- Revisions and Assignments
- I/O Assignments
- Timing Assignments
- PlatformDesigner (Qsys)
- Simulation with ModelSim
- Debugging with In-System Sources & Probes and SignalTap II

El Camino GmbH
Landshuter Str. 1
84048 Mainburg
Germany

phone: +49-8751-8787-0
fax: +49-8751-8787-10
e-mail: info@elca.de
www.elcamino.de

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